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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known **Application Number** 09/827,974 Filing Date April 6, 2001 MAR 2 0 1003 First Named Inventor Jason Souloglou Group Art Unit 2122 Technology Center 2100 **Examiner Name** To Be Assigned

(use as many sheets as necessary)

032091.0003.CIP3 Attorney Docket Number Sheet

	U.S. PATENT DOCUMENTS							
Examiner	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant	Date of Publication of	Pages, Columns, Lines, Where Relevant		
Initials*		Number	Kind Code ² (if known)	of Cited Document	Cited Document MM-DD-YYYY	Passages or Relevant Figures Appear		
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MY		AHO, et al., "Chapter 9: Code Generation," <u>Compilers- Principles, Techniques, and Tools,</u> 522-584 (1986)	
MY		AHO, et al., "Chapter 10: Code Optimization," <u>Compilers- Principles, Techniques, and Tools,</u> 603-722 (1986)	
MY		WITCHEL, et al., "Embra: Fast and Flexible Machine Simulation," Sigmetrics, 68-79 (1996)	

Examiner Signature	Date Considered	5/3/04	

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Complete if Known **Application Number** 09/827,974 4/06/01 Filing Date **First Named Inventor** Souloglou, J. Group Art Unit 2122 TBA Examiner Name Technology Conter 2100 Attorney Docket Number 032091.0003.CIP3

				U.S. PATENT DOCU	MENTS	
Examiner	Cite	U.S. Patent D	Document Kind Code ²	Name of Patentee or Applicant	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
Initials*	No.¹	Number	(if known)	Walters et al.	MM-DD-YYYY	Figures Appear
MY	AA	5,768,593		-Brown, et-al-		

	FOREIGN PATENT DOCUMENTS										
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MY	AB	Afzal, T., et al., "Motorola PowerPC Migration Tools – Emulation and Translation," Digest of Papers of the Computer society Computer Conference Compcon, US, Los Alamitos, IEEE Comp. Soc. Press, vol. Conf. 41, 25-28 February 1996 pgs. 145-150	
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MY	AD	Andrews, K. et al., "Migrating a CISC Computer Family onto RISC Via Object Code Translation," ACM Sigplan Notices, US, Association for Computing Machinery, NY, Vol. 27, No. 9, September 1992, pgs. 213-222	
MY	AE	ADL-Tabatabai A. R., et al., "Fast Effective Code Generation in a Just-in-Time Java Compiler," ACM Sigplan Notices, US, Association for Computing Machinery, NY, Vol. 33, No. 5, May 1998, pgs. 280-290	
MY	AF	"Register Assignment Technique in Computer Emulation," IBM Technical Disclosure Bulletin.US.IBM. Corp. New York, V. 37, no. 2A, February 1994 (1994-02), pg. 157	
ум	AG	Halfhill, T.R.: "Emulation: Risc's Secret Weapon," Byte, US, McGraw-Hill, Inc. St. Peterbourough, V. 19, no. 4, April 1994 (1994-04), pgs. 119-120, 122, 124	

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